



*FIG. 1*  
(PRIOR ART)

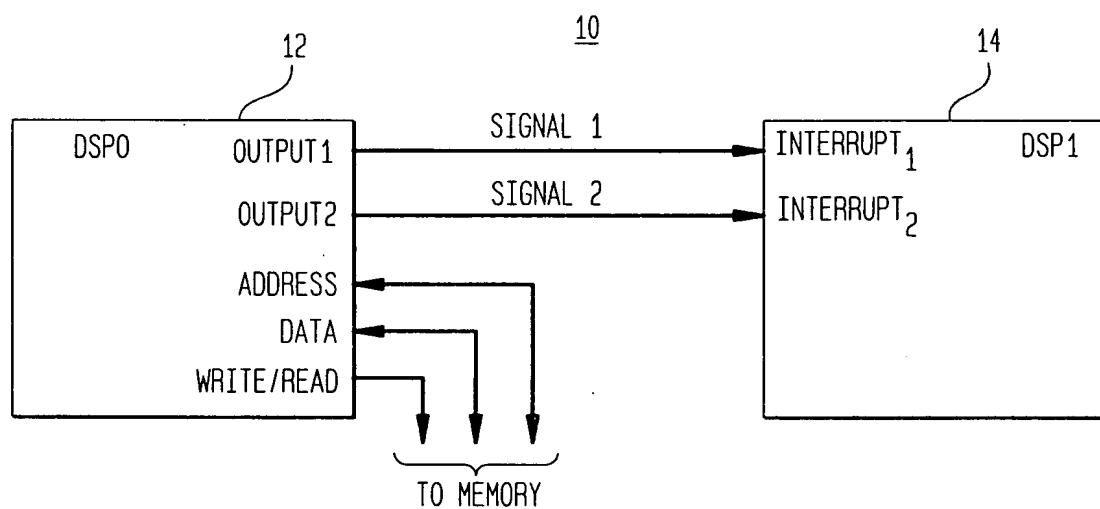




FIG. 2

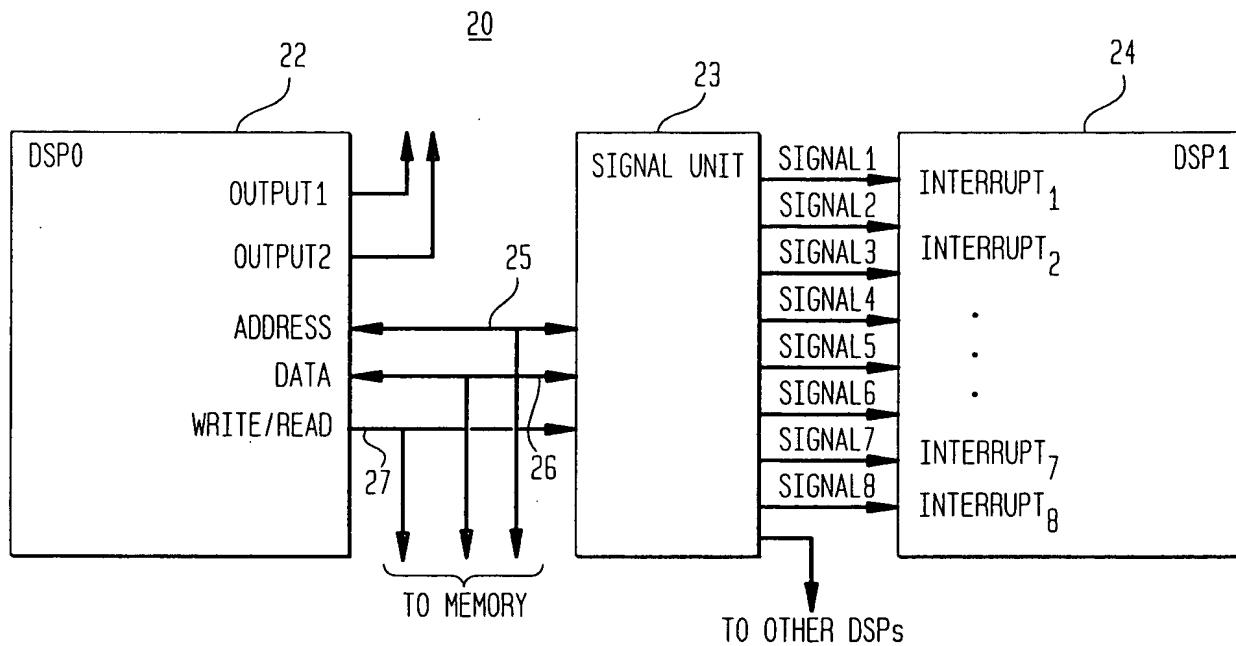


FIG. 3

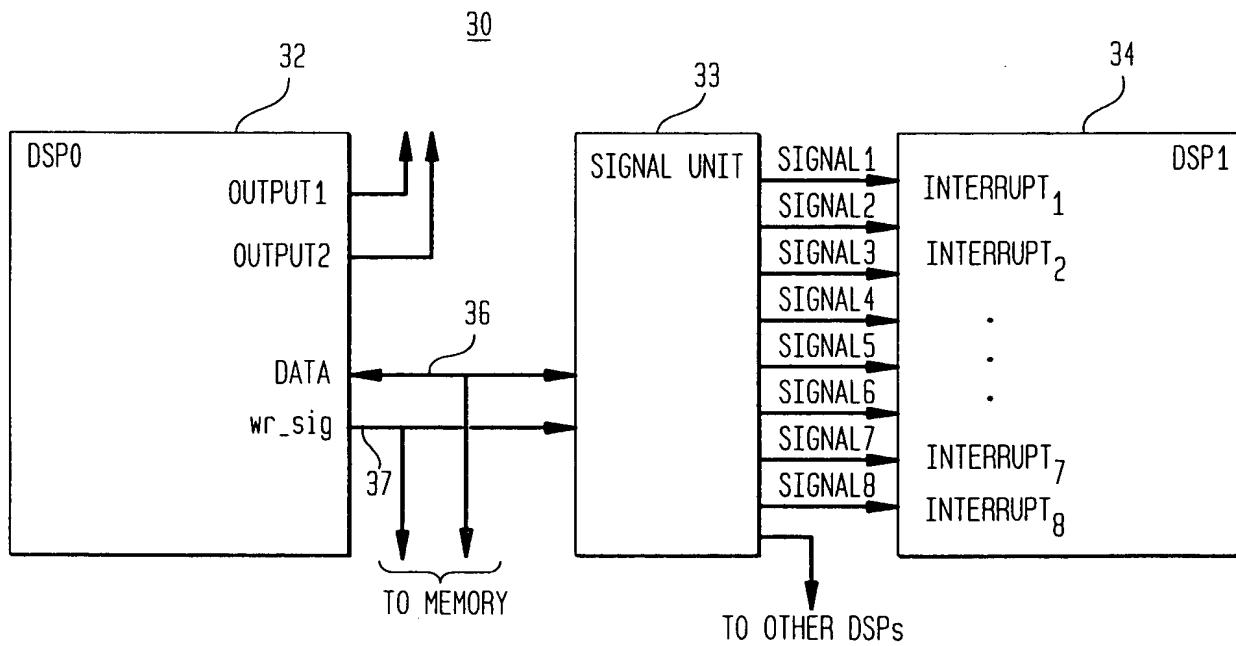
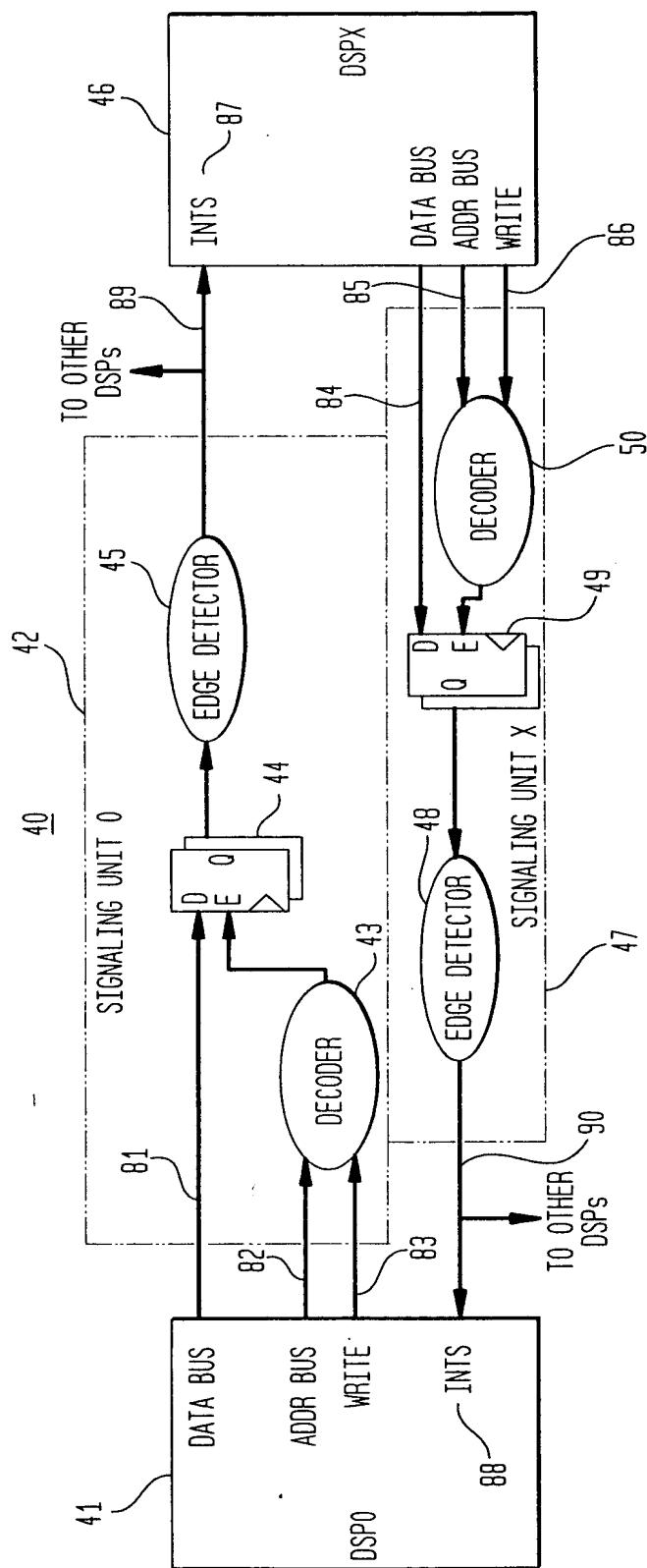




FIG. 4





Inventor: William G. Burroughs et al.  
Title: Inter-DSP Signaling in a Multiple DSP Environment  
Appln. No. 09/489,652 Filed: January 24, 2000  
Docket No. LUC-718US Customer #23122  
REPLACEMENT SHEET

FIG. 5

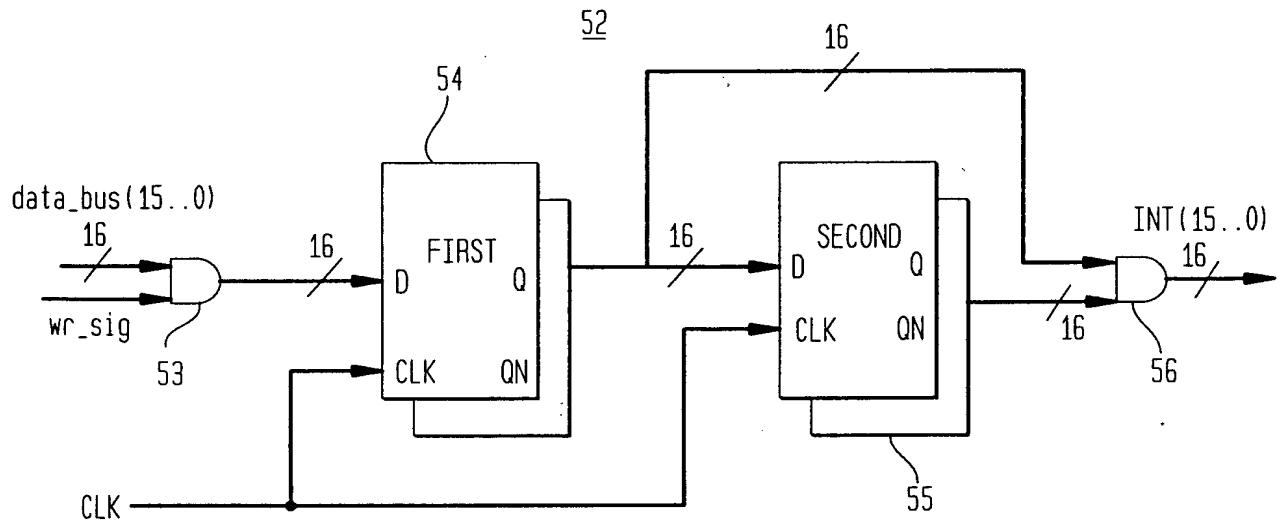


FIG. 6

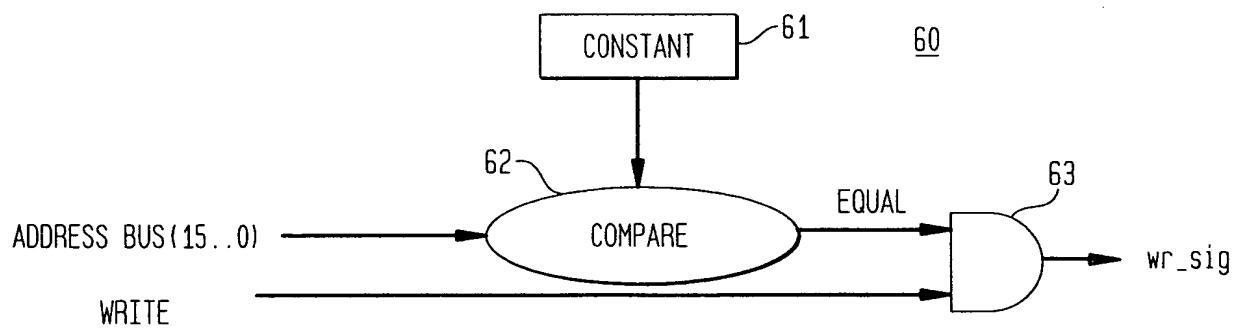




FIG. 7

